

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10060100	FILING DATE 01/31/2002	CLASS 365	SUBCLASS 49	GAU 2818	EXAMINER YOH
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**APPLICANTS: Gillingham Peter; Roth Alan;

**CONTINUING DATA VERIFIED:
THIS APPLN CLAIMS BENEFIT OF 60/343,229 12/31/2001

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 13896
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiners's initials	<i>[Signature]</i>	
TITLE : Circuit and method for reducing power usage in a content addressable memory		

U.S. DEPT. OF COMM./PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Total Claims	Print Claim for 0.6
Amount Due	Date Paid	DRAWING	
		Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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